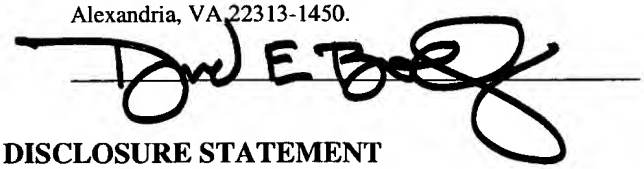
PATENT

ATTORNEY DOCKET NO. 114596-03-4000

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.:	09/385,394	Confirmation No.:	9093
Applicant:	John S. Yates, Jr., et al.		
Title:	COMPUTER WITH TWO DIFFERENT EXECUTION MODES		
Filed:	August 30, 1999		
Art Unit:	2183		
Examiner:	Richard Ellis		
Atty. Docket:	114596-03-4000		
Customer No.	38492		

I certify that this correspondence, along with any documents referred to therein, is being transmitted by facsimile on April 14, 2005 to Art Unit 2183 at FAX no. 703 872 9306, and deposited with the United States Postal Service on April 14, 2005 as First Class Mail in an envelope with sufficient postage addressed to Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant wishes to make of record the items listed on the accompanying Form PTO-1449. Applicant respectfully requests the Examiner to fully consider the items and independently ascertain their teaching before issuance of the next action, and to make them of record in the file. The Examiner is also requested to initial and return a copy of the enclosed Form PTO-1449 to evidence such consideration.

1. Applicant has listed publication dates on the attached Form PTO-1449 based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the date indicated. Applicant reserves the right to establish the patentability of the claims over any information provided herewith, and/or to prove that this information may not be prior art, and/or to prove that this information may not be enabling for the teachings purportedly offered. This Information Disclosure Statement should not be construed as a representation that information more material to the examination of this application does not exist.

2. The references listed on the enclosed Form 1449 are references that have come to light in applications listed in the Information Disclosure Statement of November 2000. For many of the listed references, pertinence is mentioned in the respective application file in which the reference came to light. Applicant has not reviewed all of them in detail. Of those that have been reviewed, none of the references are believed to be any more pertinent than the references provided in earlier IDS' and Forms 1449. However, in an abundance of caution, Applicant requests that they be considered.

3. For items other than U.S. patents listed on the enclosed Form PTO-1449 for which a copy is not already made of record in this application, a copy was previously cited by or submitted to the Patent and Trademark Office in application Serial No. 09/239,194, filed January 28, 1999, Yates et al., Executing Programs for a First Computer Architecture on a Computer of a Second Architecture, in application Serial No. 09/322,443, filed May 28, 1999, Reese et al., Profiling of Computer Programs Executing in Virtual Memory Systems, each of which is an earlier application relied on for an earlier effective filing date under 35 U.S.C. § 120.

4. The Compaq, Dean, Hank and three Intel references have been previously cited. They are cited again here with improved bibliographic information.

5. This Information Disclosure Statement is being filed at a time when a nominally-final Office Action has been mailed, but when finality of that Office Action should be withdrawn, or when prosecution may be reopened on withdrawal or reversal of all pending rejections. In the event of reopening of prosecution, entry of this Information Disclosure Statement is proper on payment of a fee pursuant to 37 C.F.R. §1.97(c)(2). Upon such entry, charge the fee due under C.F.R. §1.17(i)(1) to Deposit Account No. 23-2405, Order No. 114596-03-4000.

6. The Commissioner is hereby authorized to charge any additional fees that may be required for this Information Disclosure Statement, or credit any overpayment, to Deposit Account No. 23-2405, Order No. 114596-03-4000.

Respectfully submitted,

WILLKIE FARR & GALLAGHER LLP

Dated: April 14, 2005

By: _____

David E. Boundy
Registration No. 36,461

WILLKIE FARR & GALLAGHER LLP
787 Seventh Ave.
New York, New York 10019
(212) 728-8757
(212) 728-9757 Fax

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

ATTY. DOCKET NO.	SERIAL NO.
114596-03-4000	09/385,394
APPLICANT	
John S. Yates, Jr., et al.	
FILING DATE	GROUP ART UNIT
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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		4,722,050	01/26/1988	Lee	712	205	Mar. 27, 1986
		4,831,515	05/16/1989	Kamada	712	207	Feb. 10, 1986
		5,043,878	08/1991	Ooi	712	42	Aug. 9, 1989
		5,371,894	12/06/1994	DiBrino	714	34	Dec. 23, 1993
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		5,918,251	06/29/1999	Yamada	711	207	Dec. 23, 1996
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EXAMINER	DATE CONSIDERED

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		6,789,263	09/07/2004	Shimada	725	119	Jun. 18, 1997
		6,826,748	11/30/2004	Hohensee	717	130	Jun. 24, 1999

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		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	TRANSLATION NO
		WO 99/08188 Kelly	02/18/1999	PCT/US	G06F	11/00		

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Papers, Etc.)*

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			Intel Corporation, Intel Architecture Software Developer's Manual, Volume 1: Basic Architecture, pages 3-10 to 3-13, 7-1 to 7-3, 7-20 to 7-25; Volume 2: Instruction Set Reference, pages 3-60 to 3-63, 3-240 to 3-251; Volume 3: System Programming Guide, pages 11-1 to 11-3, 11-10 to 11-13, 14-1 to 14-3, 14-10 to 14-15 (1997)
			Intel Corporation, Pentium Processor Family Developer's Manual (1997), pages 1-1 to 1-6, 2-1 to 2-20
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			Peterson, "The Profile Naming Service," ACM Transactions on Computer Systems (TOCS), v.6 n.4, p.341-364 (Nov. 1988)

EXAMINER	DATE CONSIDERED